

SANYO Semiconductors DATA SHEET

MCH3211-

NPN Epitaxial Planar Silicon Transistor

DC / DC Converter Applications

Applications

· Relay drivers, lamp drivers, motor drivers, flash.

Features

- · Adoption of MBIT processes.
- · High current capacitance.
- · Low collector-to-emitter saturation voltage.
- · High-speed switching.
- Ultrasmall package facilitates miniaturization in end products (mounting height: 0.85mm).
- · High allowable power dissipation.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		15	V
Collector-to-Emitter Voltage	VCEO		10	V
Emitter-to-Base Voltage	VEBO		5	V
Collector Current	IC		3	Α
Collector Current (Pulse)	ICP		5	Α
Base Current	IΒ		600	mA
Collector Dissipation	PC	Mounted on a ceramic board (600mm ² X0.8mm)	0.8	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector Cutoff Current	ICBO	V _{CB} =12V, I _E =0			0.1	μΑ
Emitter Cutoff Current	IEBO	VEB=4V, IC=0			0.1	μΑ
DC Current Gain	hFE	V _{CE} =2V, I _C =500mA	600			
Gain-Bandwidth Product	fΤ	V _{CE} =2V, I _C =500mA		380		MHz
Output Capacitance	Cob	VCB=10V, f=1MHz		23		pF

Marking: CG Continued on next page.

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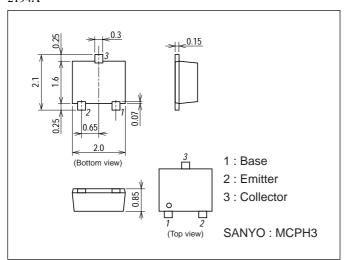
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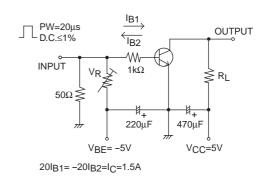
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Emitter Saturation Voltage	VCE(sat)1	IC=1.5A, IB=30mA		100	150	mV
	V _{CE} (sat)2	IC=3A, IB=60mA		180	270	mV
Base-to-Emitter Saturation Voltage	VBE(sat)	IC=1.5A, IB=30mA		0.85	1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	IC=10μA, IE=0	15			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=1mA, RBE=∞	10			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	IE=10μA, IC=0	5			V
Turn-ON Time	ton	See specified Test Circuit.		30		ns
Storage Time	t _{stg}	See specified Test Circuit.		210		ns
Fall Time	tf	See specified Test Circuit.		11		ns

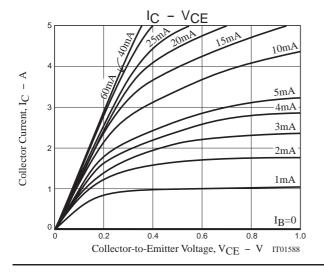
Package Dimensions

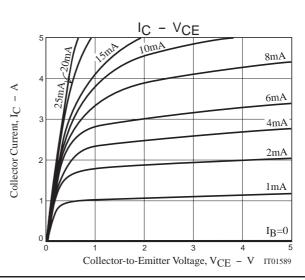
unit : mm 2194A

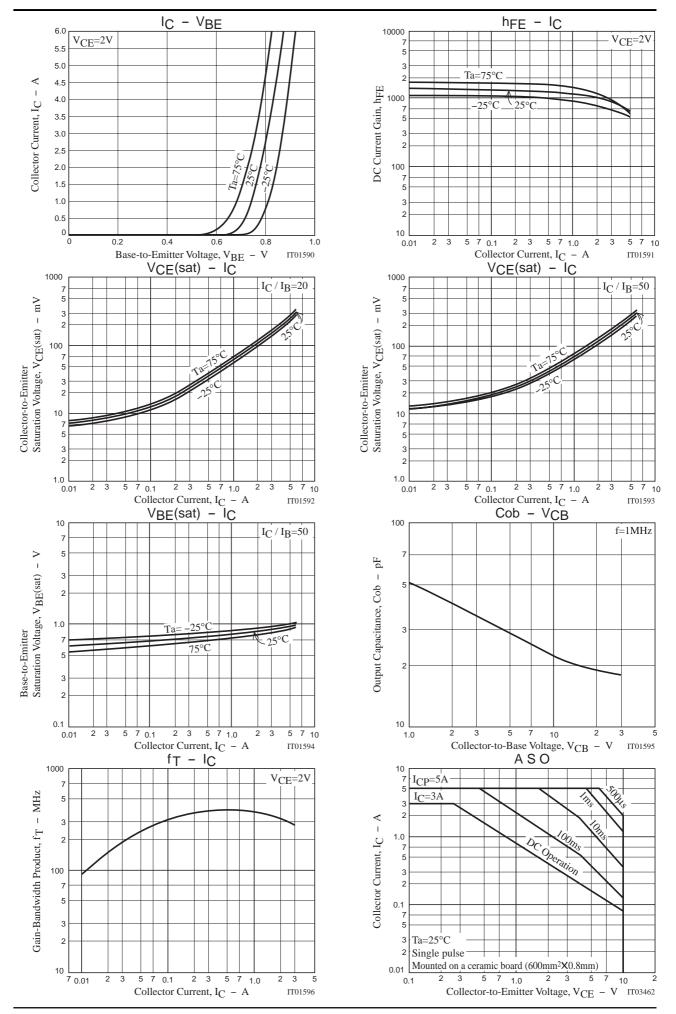


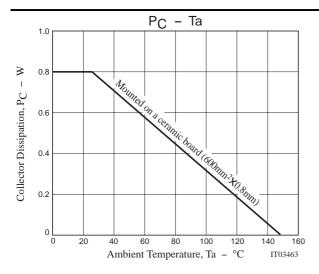
Switching Time Test Circuit











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