

BFQ540 NPN wideband transistor Rev. 05 — 21 March 2013

**Product data sheet** 

# 1. Product profile

## 1.1 General description

NPN wideband transistor in a SOT89 plastic package.

## **1.2 Features and benefits**

- High gain
- High output voltage
- Low noise

- Gold metallization ensures excellent reliability
- Low thermal resistance.

## **1.3 Applications**

VHF, UHF and CATV amplifiers.

## 1.4 Quick reference data

### Table 1. Quick reference data

Table I.	Quick reference uata					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter			20	V
V <sub>CES</sub>	collector-emitter voltage	R <sub>BE</sub> = 0			15	V
V <sub>EBO</sub>	emitter-base voltage	open collector			2.5	V
I <sub>C</sub>	collector current (DC)				120	mA
P <sub>tot</sub>	total power dissipation	$T_s \le 60 \ ^\circ C$	<u>[1]</u>		1.2	W
h <sub>FE</sub>	DC current gain	$I_C = 40 \text{ mA}; V_{CE} = 8 \text{ V};$ $T_j = 25 \text{ °C}$	100	120	250	
f <sub>T</sub>	transition frequency	$I_C = 40 \text{ mA}; V_{CE} = 8 \text{ V};$ f = 1 GHz; T <sub>amb</sub> = 25 °C		9		GHz
$ s_{21} ^2$	insertion power gain	$\label{eq:IC} \begin{array}{l} I_{C} = 40 \text{ mA};  \text{V}_{CE} = 8  \text{V}; \\ f = 900  \text{MHz};  \text{T}_{amb} = 25 ^{\circ}\text{C} \end{array}$	12	13		dB
F	noise figure	$I_{C} = 40 \text{ mA}; V_{CE} = 8 \text{ V};$ f = 900 MHz; $\Gamma_{S} = \Gamma_{opt}$		1.9	2.4	dB

[1]  $T_s$  is the temperature at the soldering point of the collector pin.



### **Pinning information** 2.

Table 2.	Pinning		
Pin	Description	Simplified outline	Graphic symbol
1	emitter		
2	collector		2 
3	base		31 1 2

### **Ordering information** 3.

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
BFQ540	-	plastic surface-mounted package; collector pad for good heat transfer; 3 leads	SOT89		

#### Marking 4.

Table 4.	Marking codes	
Type num	ber	Marking code
BFQ540		N4

### **Limiting values** 5.

#### Table 5. **Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		20	V
V <sub>CES</sub>	collector-emitter voltage	$R_{BE} = 0$		15	V
V <sub>EBO</sub>	emitter-base voltage	open collector		2.5	V
I <sub>C</sub>	collector current (DC)			120	mA
P <sub>tot</sub>	total power dissipation	$T_s \le 60 \ ^\circ C$		1.2	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	operating junction temperature			175	°C

#### **Thermal characteristics** 6.

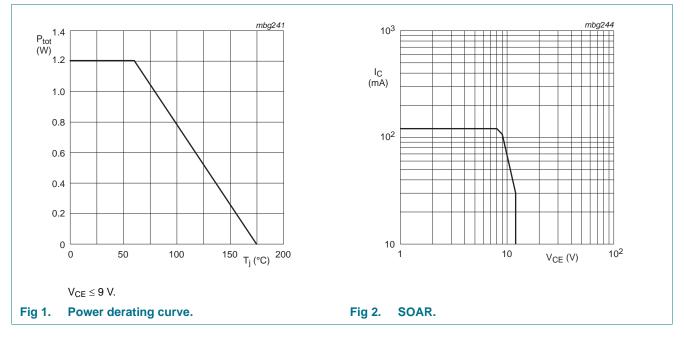
Symbol	Parameter	Conditions	Тур	Unit
R <sub>th j-s</sub>	thermal resistance from junction to soldering point	$T_s \le 60 \text{ °C}; P_{tot} = 1.2 \text{ W}$	95	K/W
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# **NXP Semiconductors**

# **NPN** wideband transistor

**BFQ540** 



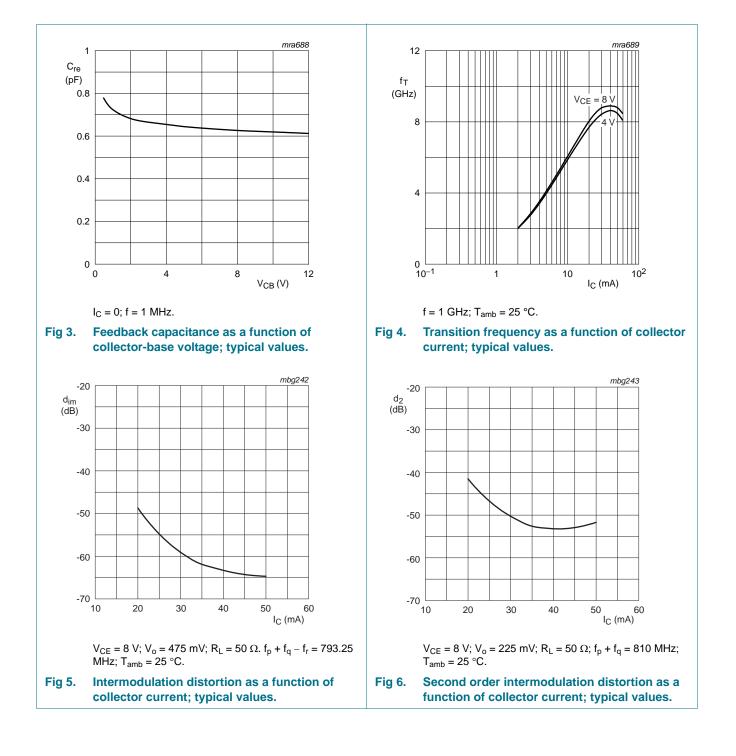
### **Characteristics** 7.

# Table 7.Characteristics $T_1 = 25 \ \odot$ unless otherwise s

Symbol	Parameter	Conditions	Mi	n Typ	Мах	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	open emitter; $I_C = 10 \ \mu A$ ; $I_E = 0$	20	1		V
V <sub>(BR)CES</sub>	collector-emitter breakdown voltage	$R_{BE} = 0; I_C = 40 \ \mu A$	15			V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	$I_{E} = 100 \ \mu A; \ I_{C} = 0$	2			V
I <sub>CBO</sub>	collector-base leakage current	$V_{CB} = 8 V; I_E = 0$			50	nA
I <sub>EBO</sub>	emitter-base leakage current	$V_{CB} = 1 V; I_C = 0$			200	nA
h <sub>FE</sub>	DC current gain	$I_{C} = 40 \text{ mA}; V_{CE} = 8 \text{ V}$	10	0 120	250	
f <sub>T</sub>	transition frequency	$I_{C}$ = 40 mA; $V_{CE}$ = 8 V; $f_{m}$ = 1 GHz		9		GHz
C <sub>e</sub>	emitter capacitance	$\begin{split} I_{C} &= i_{e} = 0; \ V_{EB} = 0.5 \ V; \\ f &= 1 \ MHz \end{split}$		2		pF
C <sub>re</sub>	feedback capacitance	$I_{C} = 0; V_{CE} = 8 V; f = 1 MHz$		0.9		pF
$ s_{21} ^2$	insertion power gain	$\label{eq:lc} \begin{array}{l} I_C = 40 \text{ mA};  V_{CE} = 8  \text{V}; \\ f = 900  \text{MHz};  T_{amb} = 25 ^\circ\text{C} \end{array}$	12	13		dB
Vo	output voltage		[1]	500		mV
			[2]	350		mV
d <sub>2</sub>	second order intermodulation distortion		<u>[3]</u>		-53	dB
F	noise figure	$I_C = 40 \text{ mA}; \text{ VCE} = 8 \text{ V};$ f = 900 MHz; $\Gamma_S = \Gamma_{opt}$		1.9	2.4	dB
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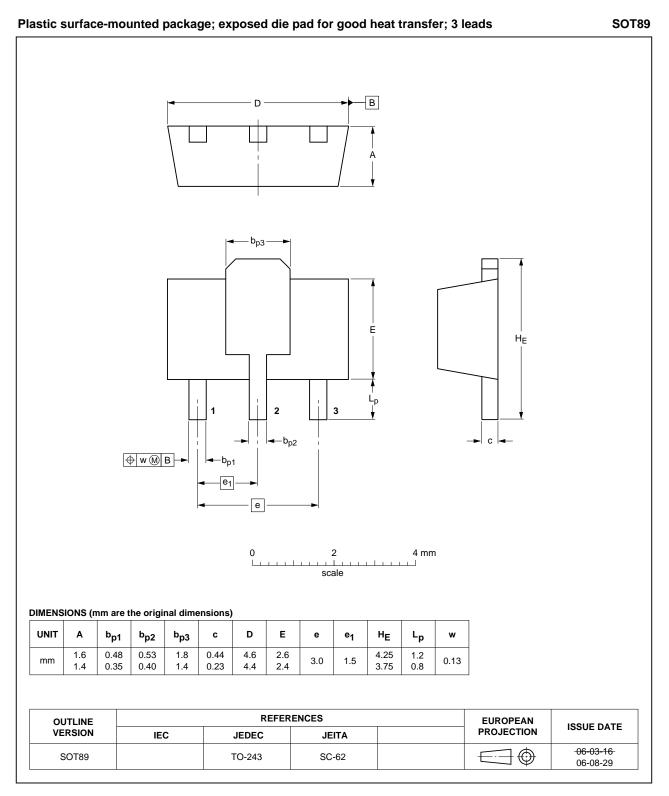
- [1]  $d_{im} = -60 \text{ dB}$  (DIN45004B);  $V_{CE} = 8 \text{ V}$ ;  $I_C = 40 \text{ mA}$ ;  $R_L = 50 \Omega$ ;  $V_p = V_o$ ;  $V_q = V_o 6 \text{ dB}$ ;  $V_r = V_o 6 \text{ dB}$ ;  $f_p = 795.25 \text{ MHz}$ ;  $f_q = 803.25 \text{ MHz}$ ;  $f_r = 805.5 \text{ MHz}$ ; measured at  $f_p + f_q f_r = 793.25 \text{ MHz}$ .
- [2]  $d_{im} = -60 \text{ dB}$  (DIN 45004B);  $I_C = 40 \text{ mA}$ ;  $V_{CE} = 8 \text{ V}$ ;  $R_L = 50 \Omega$ ;  $V_p = V_q = V_o$ ;  $f_p = 806 \text{ MHz}$ ;  $f_q = 810 \text{ MHz}$ ; measured at  $2f_p f_q = 802 \text{ MHz}$ .
- [3]  $I_C = 40 \text{ mA}$ ;  $V_{CE} = 8 \text{ V}$ ;  $R_L = 50 \Omega$ ;  $V_p = V_q = 225 \text{ mV}$ ;  $f_p = 250 \text{ MHz}$ ;  $f_q = 560 \text{ MHz}$ ; measured at  $f_p + f_q = 810 \text{ MHz}$ .



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# 8. Package outline



### Fig 7. Package outline SOT89 (TO-243).

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# 9. Revision history

Table 8. Revision	history			
Document ID	Release date	Data sheet status	Change notice	Supersedes
BFQ540 v.5	20130321	Product data sheet	-	BFQ540_N_4
Modifications:	guidelines o Legal texts	of this data sheet has been of NXP Semiconductors. have been adapted to the n utline drawings have been u updated.	ew company name whe	ere appropriate.
BFQ540_N_4	20070925	Product data sheet	-	BFQ540_3
BFQ540_3 (9397 750 07064)	20000523	Product specification		BFQ540_2
BFQ540_2 (9397 750 04296)	19980827	Product specification		BFQ540_1
BFQ540_1	19950904	Product specification		-

# **10. Legal information**

## **10.1 Data sheet status**

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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