NDPL180N10B

Advance Information

Power MOSFET 100V, 3.0mΩ, 180A, N-Channel

ON Semiconductor®

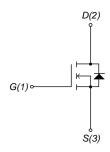
http://onsemi.com

Features

- Ultra Low On-Resistance
- Low Gate Charge
- Pb-free and RoHS Compliance
- High Speed Switching
- 100% Avalanche Tested

Electrical Connection

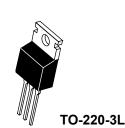
N-channel

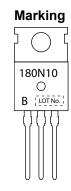


Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V _{DSS}	100	V
Gate to Source Voltage	VGSS	±20	V
Drain Current (DC)	ID	180	Α
Drain Current (DC) Limited by Package	I _{DL}	100	Α
Drain Current (Pulse)	IDP	600	Α
PW≤10μs, duty cycle≤1%			
Power Dissipation	PD	2.1	W
Tc=25°C		200	
Junction Temperature	Tj	175	°C
Storage Temperature	Tstg	– 55 to	°C
		+175	
Source Current (Body Diode)	IS	100	Α
Avalanche Energy (Single Pulse) *1	EAS	451	mJ
Lead Temperature for Soldering	TL	260	°C
Purposes, 3mm from Case for 10 Seconds			





Thermal Resistance Ratings

Parameter	Symbol	Value	Unit	
Junction to Case Steady State	$R_{\theta JC}$	0.75	0000	
Junction to Ambient *2	$R_{\theta JA}$	71.4	°C/W	

Note: *1 V_{DD} =48V, L=100 μ H, I_{AV} =70A (Fig.1)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

This document contains information on a new product. Specifications and information herein are subject to change without notice.

ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

^{*2} Insertion mounted

NDPL180N10B

Electrical Characteristics at Ta = 25 °C

Parameter	O. made al	Conditions	Value			Unit
Parameter	Symbol		min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	I _D =10mA, V _{GS} =0V	100			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =100V, V _{GS} =0V			10	μΑ
Gate to Source Leakage Current	IGSS	V _{GS} =±20V, V _{DS} =0V			±200	nA
Gate Threshold Voltage	V _{GS} (th)	V _{DS} =10V, I _D =1mA	2		4	V
Forward Transconductance	9FS	V _{DS} =10V, I _D =50A		150		S
Challe Davin to Course On Chata Basinton	R _{DS} (on)1 I _D =50A, V _{GS} =15V		2.5	3.0	mΩ	
Static Drain to Source On-State Resistance	R _{DS} (on)2	I _D =50A, V _{GS} =10V		2.7	3.3	mΩ
Input Capacitance	Ciss			6,950		pF
Output Capacitance	Coss	V _{DS} =50V, f=1MHz		3,000		pF
Reverse Transfer Capacitance	Crss			15		pF
Turn-ON Delay Time	t _d (on)	See Fig.2		95		ns
Rise Time	t _r			320		ns
Turn-OFF Delay Time	t _d (off)			185		ns
Fall Time	tf			130		ns
Total Gate Charge	Qg			95		nC
Gate to Source Charge	Qgs	V _{DS} =48V, V _{GS} =10V, I _D =100A		31		nC
Gate to Drain "Miller" Charge	Qgd			26		nC
Forward Diode Voltage	V _{SD}	I _S =100A, V _{GS} =0V		0.9	1.5	V
Reverse Recovery Time	t _{rr}	See Fig.3		150		ns
Reverse Recovery Charge	Q _{rr}	I _S =100A, V _{GS} =0V, di/dt=100A/μs		580		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Fig.1 Unclamped Inductive Switching Test Circuit

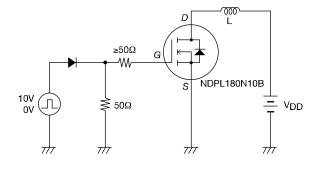


Fig.2 Switching Time Test Circuit

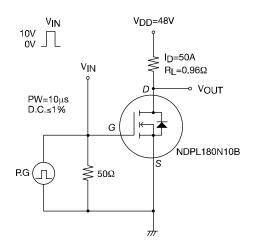
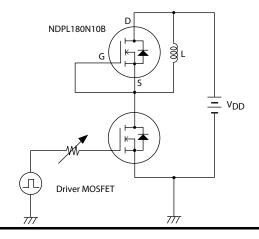
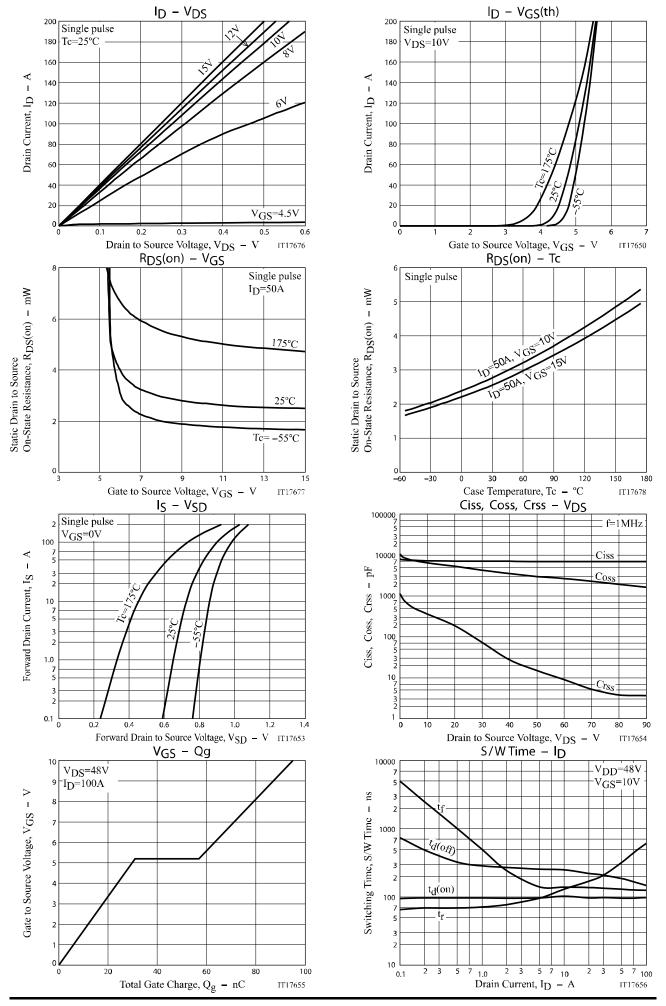
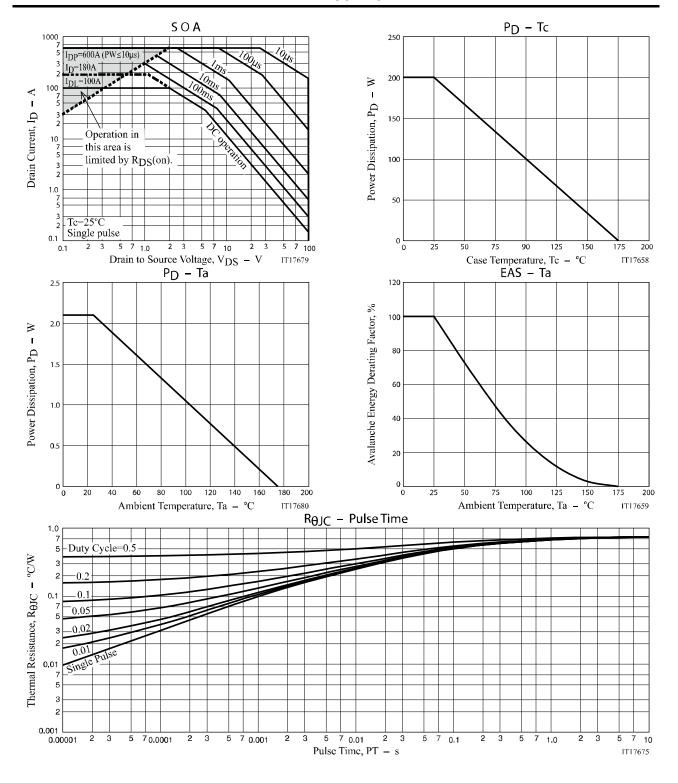


Fig.3 Reverse Recovery Time Test Circuit





NDPL180N10B



Package Dimensions

NDPL180N10BG

TO-220, 3-Lead / TO-220-3L

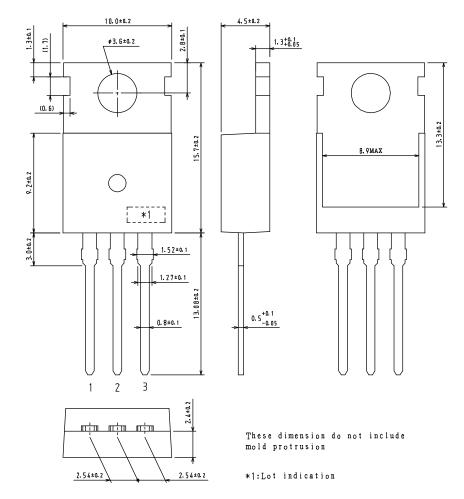
CASE 221AU ISSUE O

Unit: mm

1: Gate

2: Drain

3: Source



Ordering & Package Information

Device	Package	Shipping	note
NDPL180N10BG	TO-220-3L SC-46 TO-220AB	50 pcs. / tube	Pb-Free

Note on usage: Since the NDPL180N10B is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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