TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSIII)

TPC8108

Lithium Ion Battery Applications Notebook PC Applications Portable Equipment Applications

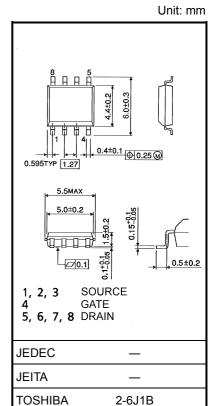
- Small footprint due to small and thin package
- Low drain-source ON resistance: $RDS(ON) = 9.5 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: $|Y_{fs}| = 24 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$
- Enhancement-mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V (V}_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA})$

Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	-30	V
Drain-gate voltage (F	$R_{GS} = 20 \text{ k}\Omega$)	V_{DGR}	-30	٧
Gate-source voltage		V_{GSS}	±20	V
Drain current	DC (Note 1)	ΙD	-11	Α
Brain current	Pulse (Note 1)	I_{DP}	-44	Α
Drain power dissipati	on $(t = 10 \text{ s})$ (Note 2a)	P_{D}	1.9	W
Drain power dissipati	on $(t = 10 s)$ (Note 2b)	P_{D}	1.0	W
Single pulse avalanc	he energy (Note 3)	E _{AS}	157	mJ
Avalanche current		I _{AR}	-11	Α
Repetitive avalanche	energy Note 2a) (Note 4)	E _{AR}	0.19	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature	range	T _{stg}	-55 to 150	°C

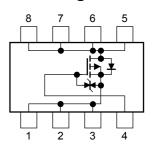
Note: For (Note 1), (Note 2), (Note 3) and (Note 4), please refer to the next page.

This transistor is an electrostatic sensitive device. Please handle with caution.



Weight: 0.080 g (typ.)

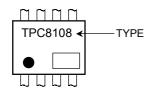
Circuit Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W	
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W	

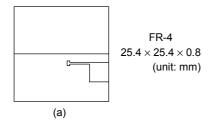
Marking (Note 5)

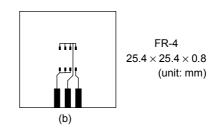


Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)





Note 3: $V_{DD} = -24~V,~T_{ch} = 25^{\circ}C$ (initial), L = 1.0 mH, R_G = 25 $\Omega,~I_{AR} = -11~A$

Note 4: Repetitive rating; pulse width limited by maximum channel temperature

Note 5: • on lower left of the marking indicates Pin 1.

shows lot number. (year of manufacture: last decimal digit of the year of manufacture, month of manufacture: January to December are denoted by letters A to L respectively.)

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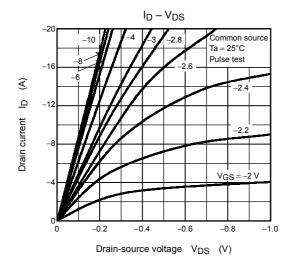
Electrical Characteristics (Ta = 25°C)

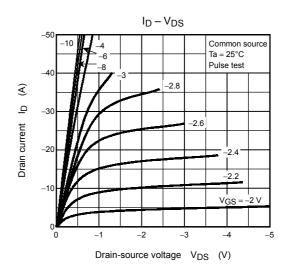
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cur	rrent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ	
Drain cut-OFF cu	ırrent	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μΑ	
Drain-source bre	akdown voltago	V _{(BR)DSS}	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-30	_	_	V	
Dialii-source bre	akdown voltage	V _{(BR)DSX}	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	-15	- ±10 10 -30 -15	V		
Gate threshold v	oltage	V _{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-0.8	_	-2.0	V	
Drain-source ON	rogistance	De a casin	$V_{GS} = -4 \text{ V}, I_D = -5.5 \text{ A}$	— 18.5 2		23	mΩ	
Dialii-Source ON	resistance	R _{DS} (ON)	$V_{GS} = -10 \text{ V}, I_D = -5.5 \text{ A}$	— — ±10 — — —	11177			
Forward transfer admittance		Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -5.5 \text{ A}$	12	24	_	S	
Input capacitance		C _{iss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	3510	_	pF	
Reverse transfer capacitance		C _{rss}		_	250	_		
Input capacitance		Coss		_	600	_		
	Rise time	t _r	0.V.7.	_	7	_	- ns	
Cuitabing time	Turn-ON time	t _{on}	$V_{GS} = 0$ V $V_{GS} = 0$ V_{OUT} $V_{OUT} = 0$	_	16	±10 -102.0 23		
Switching time	Fall time	t _f	R _L = 2.7	_	66	_		
	Turn-OFF time	t _{off}	$V_{DD} \simeq -15 \text{ V}$ Duty \leq 1%, $t_W = 10 \mu\text{s}$	_	230	_		
Total gate charge (gate-source plus		Qg	$V_{DD} \simeq -24 \text{ V}, V_{GS} = -10 \text{ V},$		77		_	
Gate-source charge 1		Q _{gs1}	$I_D = -11 \text{ A}$		7.0		nC	
Gate-drain ("mille	er") charge	Q _{gd}			20			

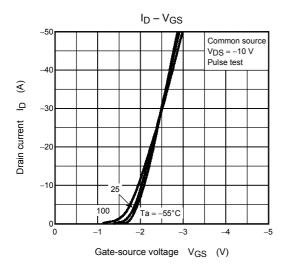
Source-Drain Ratings and Characteristics (Ta = 25°C)

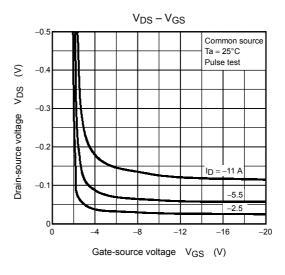
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	-44	Α
Forward voltage (diode)			V_{DSF}	$I_{DR} = -11 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.2	V

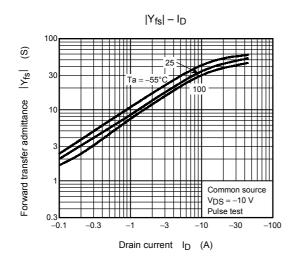
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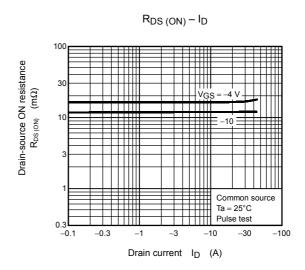


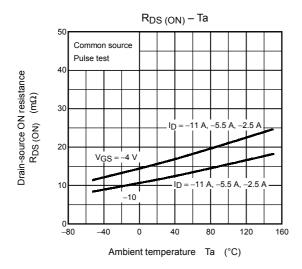


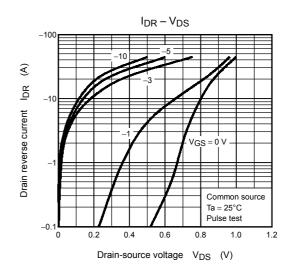


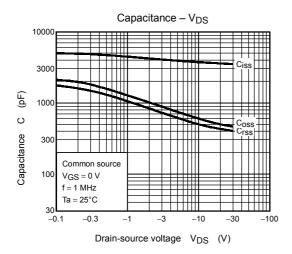


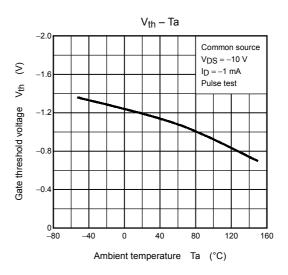


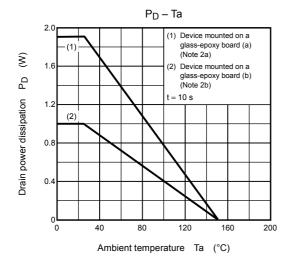


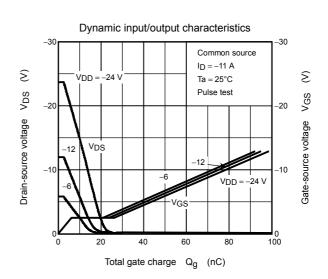


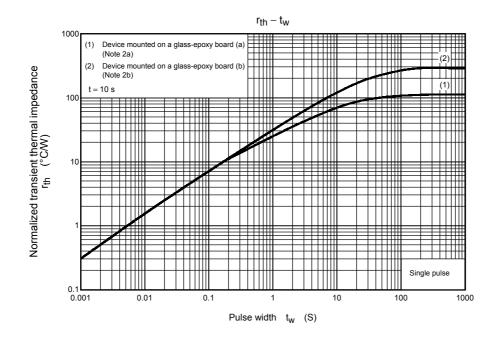


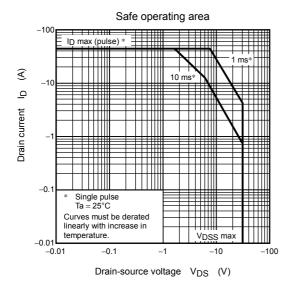












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