

FDC6325L Integrated Load Switch

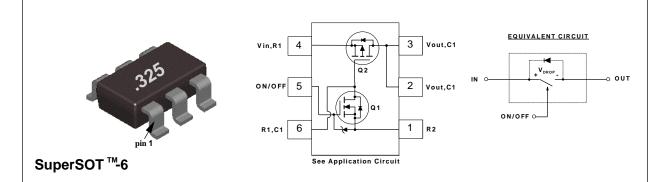
General Description

This device is particularly suited for compact power management in portable electronic equipment where 2.5V to 8V input and 1.8A output current capability are needed. This load switch integrates a small N-Channel power MOSFET (Q1) which drives a large P-Channel power MOSFET (Q2) in one tiny SuperSOT $^{\text{TM}}$ -6 package.

Features

- V_{DROP} =0.2V @ V_{IN} =5V, I_{L} =1.5A. $R_{(ON)}$ = 0.13Ω V_{DROP} =0.2V @ V_{IN} =3.3V, I_{L} =1.2A. $R_{(ON)}$ = 0.16Ω V_{DROP} =0.2V @ V_{IN} =2.5V, I_{L} =1A. $R_{(ON)}$ = 0.18Ω.
- SuperSOTTM-6 package design using copper lead frame for superior thermal and electrical capabilities.





Absolute Maximum Ratings T_A = 25°C unless otherwise noted

Symbol	Parameter	FDC6325L	Units
V _{IN}	Input Voltage Range	2.5 - 8	V
V _{ON/OFF}	On/Off Voltage Range	1.5 - 8	V
I _L	Load Current - Continuous (Note 1)	1.8	А
	- Pulsed (Note 1 & 3)	5	
P_{D}	Maximum Power Dissipation (Note 2)	0.7	W
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to 150	℃
ESD	Electrostatic Discharge Rating MIL-STD-883D Human Body Model (100pf/1500Ohm)	6	kV
THERMA	L CHARACTERISTICS		•
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 2)	180	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case (Note 2)	60	°C/W

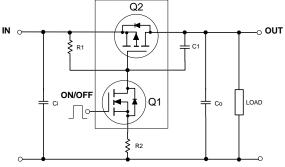
Symbol	Parameter	Min	Тур	Max	Units	
OFF CHA	RACTERISTICS	·				
I _{FL}	Forward Leakage Current	$V_{IN} = 8 \text{ V}, V_{ONOFF} = 0 \text{ V}$			1	μA
ON CHAR	ACTERISTICS (Note 3)					
V _{DROP}	Conduction Voltage Drop	$V_{IN} = 5 \text{ V}, \ V_{ON/OFF} = 3.3 \text{ V}, \ I_{L} = 1.5 \text{ A}$		0.15	0.2	V
		$V_{IN} = 3.3 \text{ V}, \ V_{ONOFF} = 3.3 \text{ V}, \ I_L = 1.2 \text{ A}$		0.145	0.2	
		$V_{IN} = 2.5 \text{ V}, \ V_{ON/OFF} = 3.3 \text{ V}, \ I_{L} = 1 \text{ A}$		0.13	0.2	
R _(ON)	Q ₂ - Static On-Resistance	$V_{GS} = -5 \text{ V}, I_{D} = -1.8 \text{ A}$		0.115	0.13	Ω
		$V_{GS} = -3.3 \text{ V}, I_D = -1.6 \text{ A}$		0.13	0.16	
		$V_{GS} = -2.5 \text{ V}, I_D = -1.5 \text{ A}$		0.155	0.18	
I _L	Load Current	$V_{DROP} = 0.13 \text{ V}, V_{IN} = 5 \text{ V}, V_{ONOFF} = 3.3 \text{ V}$	1			Α
		$V_{DROP} = 0.16 \text{ V}, V_{IN} = 3.3 \text{ V}, V_{ON/OFF} = 3.3 \text{ V}$	1			
		$V_{DROP} = 0.2 \text{ V}, V_{IN} = 2.5 \text{V}, V_{ON/OFF} = 3.3 \text{ V}$	1			

Notes:

- 1. $V_{IN}=8V$, $V_{ON/OFF}=8V$, $T_A=25^{\circ}C$
- R_{gus} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface
 of the drain pins. R_{gus} is guaranteed by design while R_{gcs} is determined by the user's board design.
- 3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2.0%.

FDC6325L Load Switch Application

APPLICATION CIRCUIT



External Component Recommendation

For Co £ 1uF applications:

First select R2, 100 - 1kW, for Slew Rate control. C1 \pm 1000pF can be added in addition to R2 for further In-rush current control.

Then select R1 such that R1/R2 ratio maintains between 10 - 100. R1 is required to turn Q2 off. For SPICE simulation, users can download a "FDC6325L.MOD" Spice model from Fairchild Web Site at www.fairchildsemi.com

Typical Electrical Characteristics ($T_A = 25$ $^{\circ}C$ unless otherwise noted)

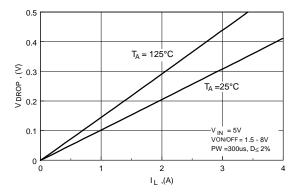


Figure 1. Conduction Voltage Drop Variation with Load Current.

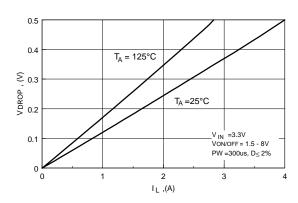


Figure 2. Conduction Voltage Drop Variation with Load Current.

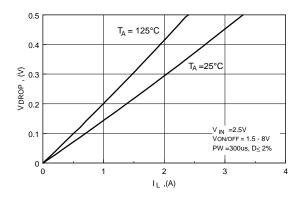


Figure 3. Conduction Voltage Drop Variation with Load Current.

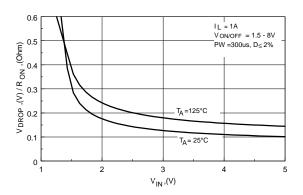


Figure 4. On-Resistance Variation with Input Voltage.

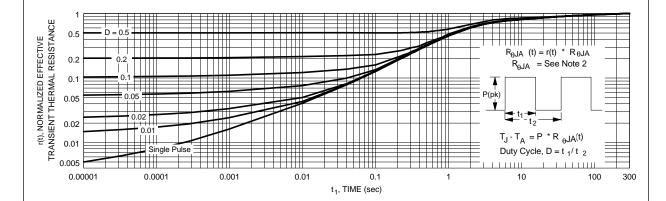
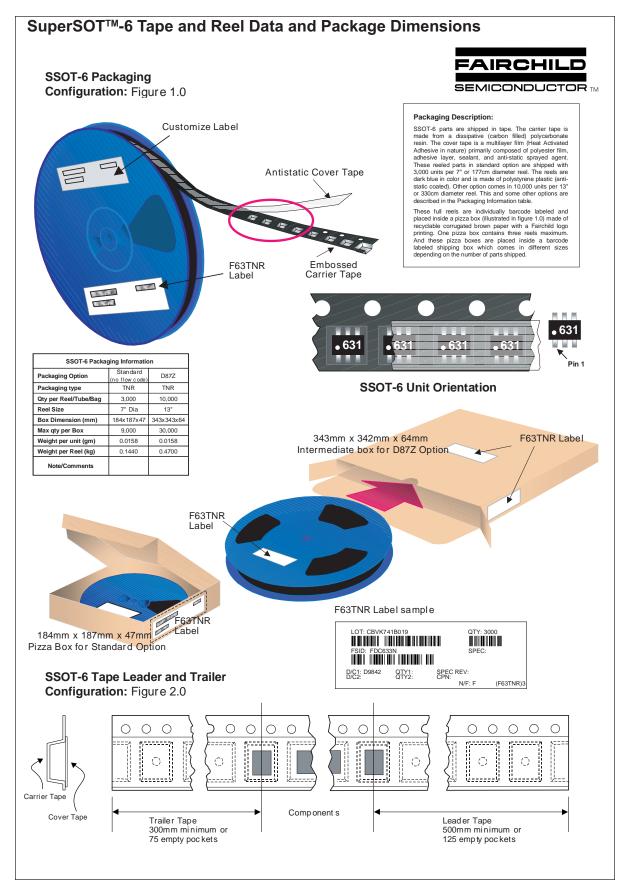
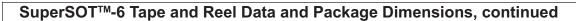


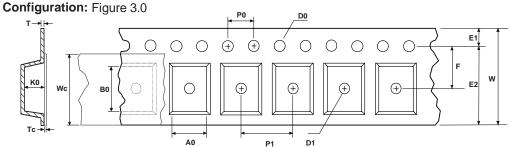
Figure 5. Transient Thermal Response Curve.

Thermal characterization performed on the conditions described in Note 2. Transient thermal response will change depends on the circuit board design.





SSOT-6 Embossed Carrier Tape



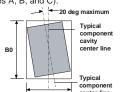


Dimensions are in millimeter														
Pkg type	Α0	В0	w	D0	D1	E1	E2	F	P1	P0	K0	т	Wc	Тс
SSOT-6 (8mm)	3.23 +/-0.10	3.18 +/-0.10	8.0 +/-0.3	1.55 +/-0.05	1.125 +/-0.125	1.75 +/-0.10	6.25 min	3.50 +/-0.05	4.0 +/-0.1	4.0 +/-0.1	1.37 +/-0.10	0.255 +/-0.150	5.2 +/-0.3	0.06 +/-0.02

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



Sketch A (Side or Front Sectional View)
Component Rotation

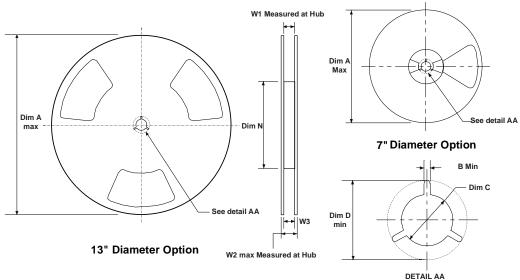


Sketch B (Top View)
Component Rotation



Sketch C (Top View)
Component lateral movement

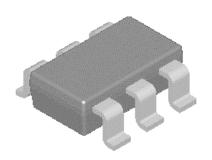
SSOT-6 Reel Configuration: Figure 4.0

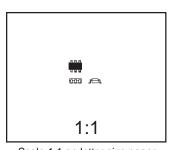


Dimensions are in inches and millimeters									
Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
8mm	7" Dia	7.00 177.8	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	2.165 55	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9
8mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	4.00 100	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9

SuperSOT™-6 Tape and Reel Data and Package Dimensions, continued

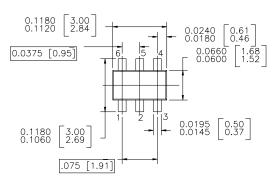
SuperSOT -6 (FS PKG Code 31, 33)

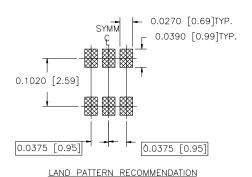




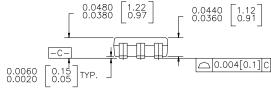
Scale 1:1 on letter size paper
Dimensions shown below are in:
inches [millimeters]

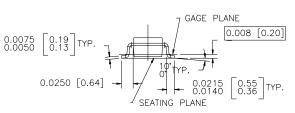
Part Weight per unit (gram): 0.0158





CONTROLLING DIMENSION IS INCH VALUES IN [] ARE MILLIMETERS





NOTES: UNLESS OTHERWISE SPECIFIED

1.0 STANDARD LEAD FINISH: 150 MICROINCHES 93.81 MICROMETERS) MINIMUM TIN / LEAD (SOLDER) ON COPPER.

 $2.0\ \mathsf{NO}\ \mathsf{JEDEC}\ \mathsf{REGISTRATION}\ \mathsf{AS}\ \mathsf{OF}\ \mathsf{JULY}\ 1996$

SUPER SOT 6 LEADS

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