TOSHIBA INSULATED GATE BIPOLAR TRANSISTOR SILICON N CHANNEL IGBT

GT50J301

HIGH POWER SWITCHING APPLICATIONS MOTOR CONTROL APPLICATIONS

Third generation IGBT

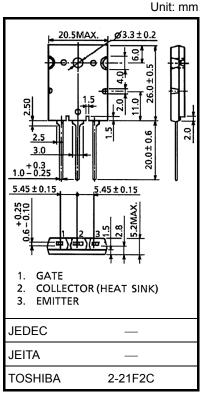
Enhancement mode type

• High speed : $t_f = 0.30\mu s$ (Max.) • Low saturation voltage : VCE (sat) = 2.7V (Max.)

• FRD included between emitter and collector

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

| CHARACTERISTIC | | SYMBOL | RATING | UNIT | |
|---|-----|------------------|---------|------|--|
| Collector-Emitter Voltage | | V _{CES} | 600 | V | |
| Gate-Emitter Voltage | | V _{GES} | ±20 | V | |
| Collector Current | DC | IC | 50 | А | |
| | 1ms | ICP | 100 | | |
| Forward Current | DC | lF | 50 | А | |
| | 1ms | I _{FM} | 100 | | |
| Collector Power Dissipation (Tc = 25°C) | | PC | 200 | W | |
| Junction Temperature | | Tj | 150 | °C | |
| Storage Temperature | | T _{stg} | -55~150 | °C | |
| Screw Torque | | _ | 0.8 | N·m | |

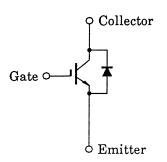


Weight: 9.75 g (typ.)

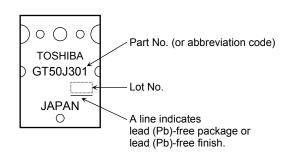
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

EQUIVALENT CIRCUIT



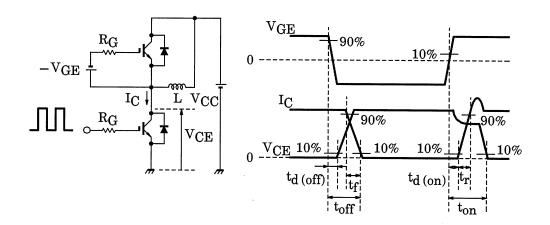
MARKING

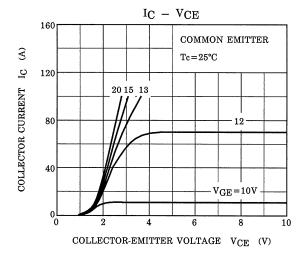


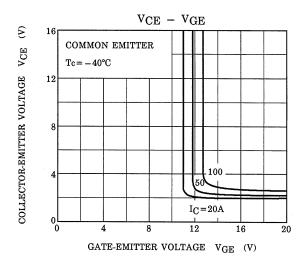
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

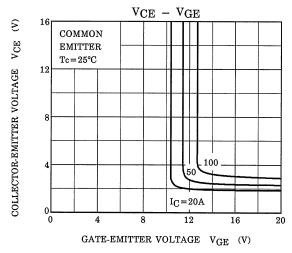
| CHARACTERISTIC | | SYMBOL | TEST CONDITION | MIN | TYP. | MAX | UNIT |
|--------------------------------------|--------------------|-----------------------|--|-----|------|-------|-----------|
| Gate Leakage Current | | I _{GES} | V _{GE} = ±20V, V _{CE} = 0 | _ | _ | ±500 | nA |
| Collector Cut-off Current | | I _{CES} | V _{CE} = 600V, V _{GE} = 0 | _ | _ | 1.0 | mA |
| Gate-Emitter Cut-off Voltage | | V _{GE} (OFF) | I _C = 5mA, V _{CE} = 5V | 5.0 | 7.0 | 8.0 | V |
| Collector-Emitter Saturation Voltage | | V _{CE} (sat) | I _C = 50A, V _{GE} = 15V | _ | 2.1 | 2.7 | V |
| Input Capacitance | | C _{ies} | V _{CE} = 10V, V _{GE} = 0 f = 1MHz | _ | 4500 | _ | pF |
| Switching Time | Turn-on delayTime | t _{d (on)} | Inductive Load $V_{CC} = 300V$ $V_{GE} = \pm 15V$ $I_{C} = 50A$ $R_{G} = 24\Omega$ | _ | 0.08 | _ | - - µs |
| | Rise Time | t _r | | _ | 0.12 | _ | |
| | Turn-on Time | t _{on} | | _ | 0.40 | _ | |
| | Turn-off delayTime | t _{d (off)} | | _ | 0.20 | _ | |
| | Fall Time | t _f | (Note 1) | _ | 0.15 | 0.30 | |
| | Turn-off Time | t _{off} | | _ | 0.50 | _ | |
| Forward Voltage | | V _F | I _F = 50A, V _{GE} = 0 | _ | 2.4 | 3.5 | V |
| Reverse Recovery Time | | t _{rr} | I _F = 50A, V _{GE} = 10V di / dt = 100A / μs | _ | 0.1 | 0.2 | μs |
| Thermal Resistance | | R _{th (j-c)} | IGBT | _ | _ | 0.625 | °C / W |
| Thermal Resistance | | R _{th (j-c)} | DIODE | _ | _ | 2.50 | °C / W |

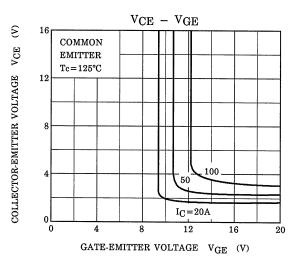
Note 1: Switching time measurement circuit and input / output waveforms

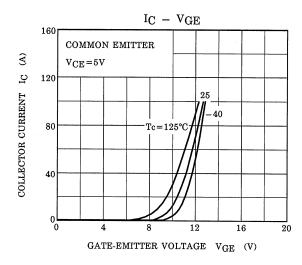


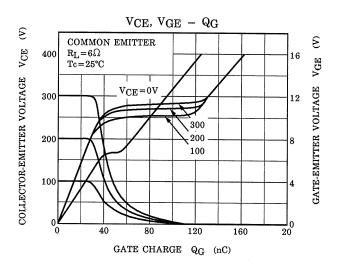




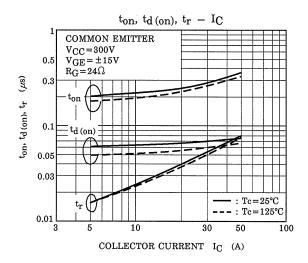


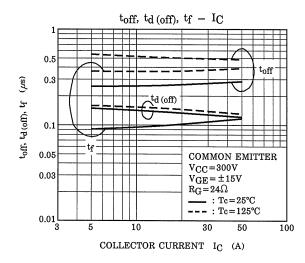


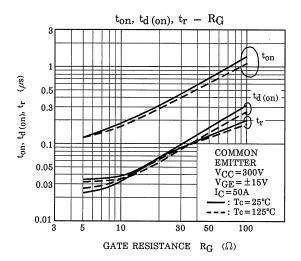


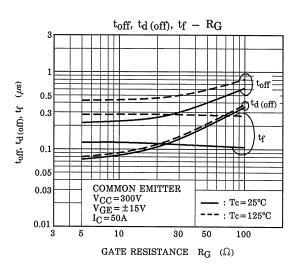


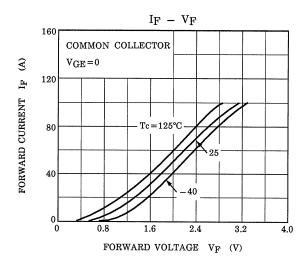
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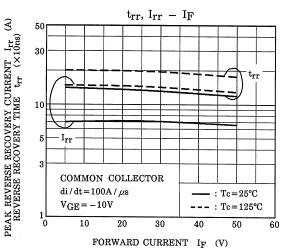


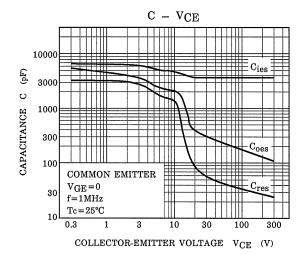


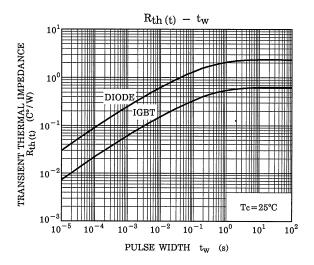


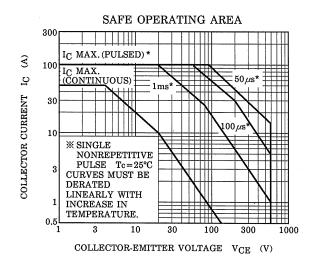


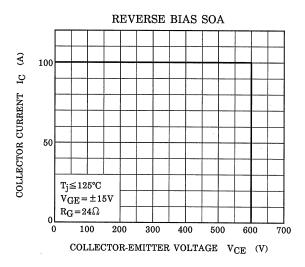












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