

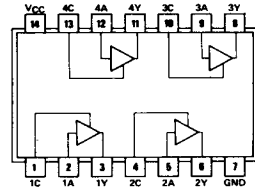
54/74 FAMILIES OF COMPATIBLE TTL CIRCUITS

PIN ASSIGNMENTS (TOP VIEWS)

QUAD GATES

426 3-STATE OUTPUTS
ACTIVE-HIGH ENABLING

positive logic: $Y = A$



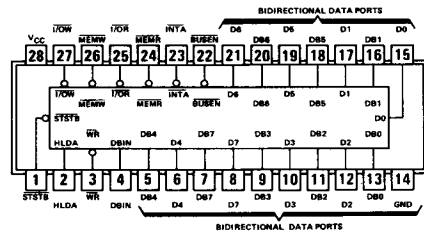
SN54426 (J, W) SN74426 (J, N)

See page 6-33

SYSTEM CONTROLLER FOR 8080A

428 BIDIRECTIONAL DATA PORTS

438 BIDIRECTIONAL DATA PORTS



SN74628 (N)
SN74638 (N)

See page 7-514

QUAD TRIDIRECTIONAL BUS TRANSCEIVERS

440 OPEN-COLLECTOR NONINVERTED OUTPUTS

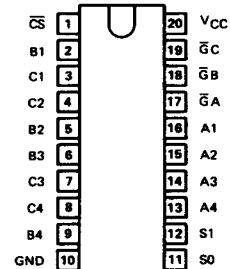
441 OPEN-COLLECTOR INVERTED OUTPUTS

442 3-STATE NONINVERTED OUTPUTS

443 3-STATE INVERTED OUTPUTS

444 3-STATE INVERTED AND NONINVERTED OUTPUTS

448 OPEN-COLLECTOR INVERTED AND NONINVERTED OUTPUTS



SN54LS440 (J) SN74LS440 (J, N)
SN54LS441 (J) SN74LS441 (J, N)
SN54LS442 (J) SN74LS442 (J, N)
SN54LS443 (J) SN74LS443 (J, N)
SN54LS444 (J) SN74LS444 (J, N)
SN54LS448 (J) SN74LS448 (J, N)

See page 7-507