

# **HD74HC280**

# 9-bit Odd/Even Parity Generator/Checker

REJ03D0606-0200 (Previous ADE-205-484) Rev.2.00 Jan 31, 2006

### **Description**

This parity generator/checker features odd/even outputs to facilitate operation of either odd or even parity applications. The word length capability is easily expanded by cascading devices.

### **Features**

• High Speed Operation:  $t_{pd}$  (Data to  $\Sigma$  Even or  $\Sigma$  Odd) = 22 ns typ ( $C_L = 50$  pF)

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage:  $V_{CC} = 2$  to 6 V

• Low Input Current: 1 µA max

• Low Quiescent Supply Current:  $I_{CC}$  (static) = 4  $\mu$ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)	
HD74HC280P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_	
HD74HC280FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)	

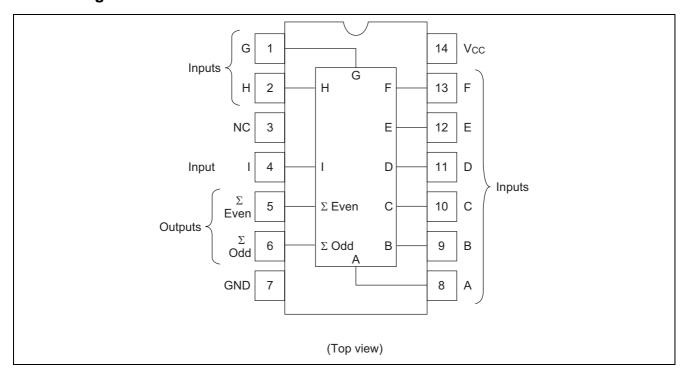
Note: Please consult the sales office for the above package availability.

#### **Function Table**

Number of inputs	Outputs					
A through I that are high	Σ Even	Σ Odd				
0, 2, 4, 6, 8	Н	L				
1, 3, 5, 7, 9	L	Н				

H: High level L: Low level

### **Pin Arrangement**



## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V
Input / Output voltage	Vin, Vout	-0.5 to V <sub>CC</sub> +0.5	V
Input / Output diode current	I <sub>IK</sub> , I <sub>OK</sub>	±20	mA
Output current	I <sub>0</sub>	±25	mA
V <sub>CC</sub> , GND current	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA
Power dissipation	P <sub>T</sub>	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

# **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	Vcc	2 to 6	V	
Input / Output voltage	V <sub>IN</sub> , V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	Та	-40 to 85	°C	
Input rise / fall time <sup>*1</sup>	t <sub>r</sub> , t <sub>f</sub>	0 to 1000	ns	V <sub>CC</sub> = 2.0 V
		0 to 500		V <sub>CC</sub> = 4.5 V
		0 to 400		$V_{CC} = 6.0 \text{ V}$

Notes: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.

# **Electrical Characteristics**

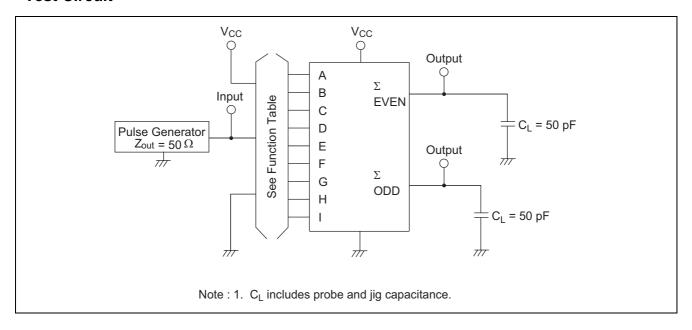
ltom	Cumbal	V 00	Т	a = 25°	С	Ta = -40	to+85°C	Unit	Test Conditions	
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit		
Input voltage	$V_{IH}$	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15	_			
		6.0	4.2	1	_	4.2				
	$V_{IL}$	2.0	_	_	0.5	_	0.5	V		
		4.5	1	1	1.35		1.35			
		6.0	_	_	1.8	_	1.8			
Output voltage	V <sub>OH</sub>	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_			
		6.0	5.9	6.0	_	5.9	_			
		4.5	4.18	_	_	4.13	_			$I_{OH} = -4 \text{ mA}$
		6.0	5.68	_	_	5.63	_			$I_{OH} = -5.2 \text{ mA}$
	V <sub>OL</sub>	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \mu A$
		4.5	_	0.0	0.1	_	0.1			
		6.0	_	0.0	0.1	_	0.1			
		4.5	_	_	0.26	_	0.33			I <sub>OL</sub> = 4 mA
		6.0	_	_	0.26	_	0.33			$I_{OL} = 5.2 \text{ mA}$
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V <sub>CC</sub> or GND	
Quiescent supply current	I <sub>CC</sub>	6.0	_	_	4.0	_	40	μА	Vin = $V_{CC}$ or GND, lout = $0 \mu A$	

# **Switching Characteristics**

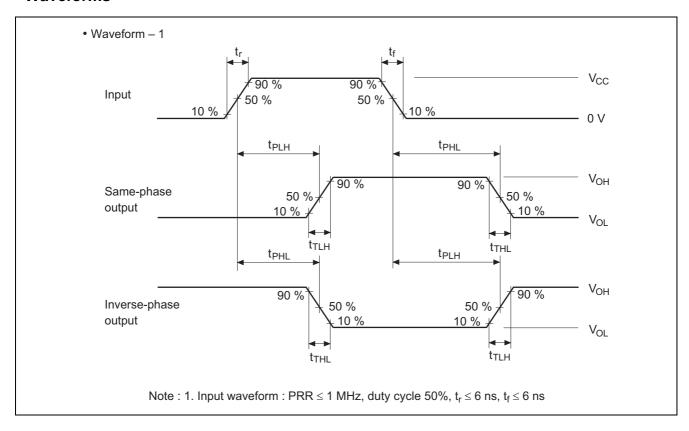
 $(C_L = 50 \text{ pF}, \text{Input } t_r = t_f = 6 \text{ ns})$ 

Item	Symbol	Symbol	V <sub>cc</sub> (V)	Т	a = 25°	С	Ta = -40	to +85°C	Unit	Test Conditions
iteiii		ACC (A)	Min	Тур	Max	Min	Max	Oilit	rest Conditions	
Propagation delay	t <sub>PLH</sub>	2.0	l	1	205	_	255	ns	Data to $\Sigma$ Even or $\Sigma$ Odd	
time	t <sub>PHL</sub>	4.5	l	22	41	_	51			
		6.0	_	_	35	_	43			
Output rise/fall	t <sub>TLH</sub>	2.0	l	1	75	_	95	ns		
time	t <sub>THL</sub>	4.5		5	15	_	19			
		6.0	_	_	13	_	16			
Input capacitance	Cin	_	1	5	10	_	10	pF		

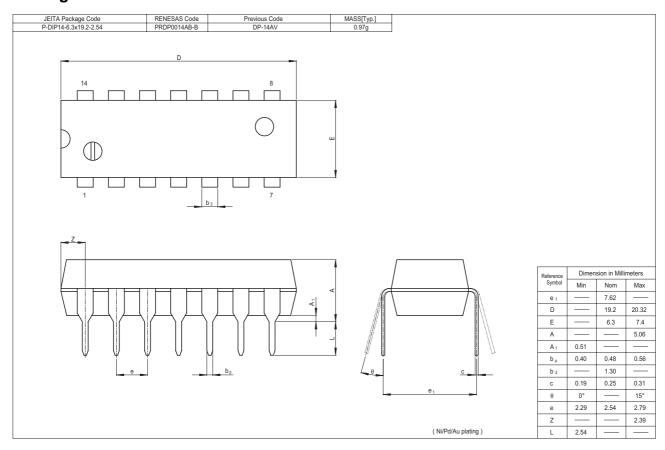
#### **Test Circuit**

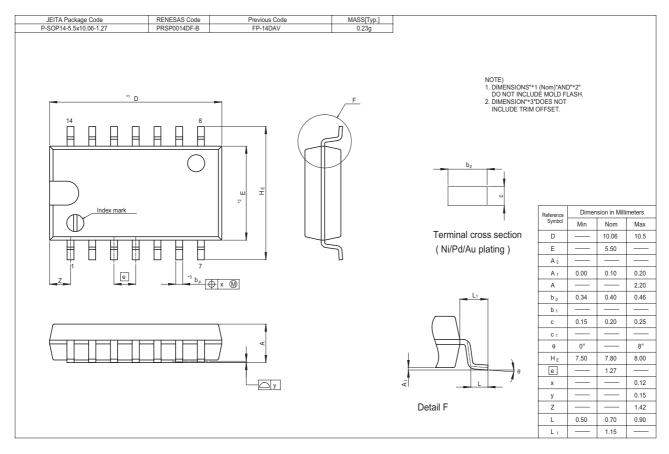


#### **Waveforms**



### **Package Dimensions**





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