

TOSHIBA MOS MEMORY PRODUCTS

1024 WORD x 4 BIT STATIC RAM

N CHANNEL SILICON GATE DEPLETION LOAD

TMM314AP

TMM314AP-1

TMM314AP-3

TMM314APL

TMM314APL-1

TMM314APL-3

DESCRIPTION

TMM314AP family is 1024 word x 4 bit high speed read write memories operated with 5 V single power supply. The memories with 6 Tr. cells are static in operation and require no clocks or refresh period and suitable for use in microprocessor application systems where high performance, low cost, simple interfacing are important design objectives.

TMM314AP family is able to be connected to

TTL directly and to drive 1 STTL or 5 LSTTLs.

TMM314AP family is fabricated with N channel silicon gate depletion load type MOS technology by ion implantation for fast speed, stable performance and reliability.

The chip is moulded in the standard 18 pin plastic package of 0.3 inch width for low cost and high density assembly.

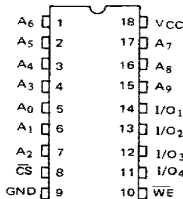
FEATURES

- Fully decoded 1024 word x 4 bit organization
- Static operation — No clocks or refresh period
- Single 5V supply voltage — $V_{CC} = 5V \pm 10\%$
- Easy memory expansion — CS input
- Three state output — Wired OR tie capability
- Inputs and outputs directly TTL compatible
- Data input/output terminal is common
- Input protected — All inputs have protection against static charge
- 2114 Type Pin compatible

- Low Power dissipation and Access time
Power and Access time (maximum value)

	Access time	Power
TMM314AP-1	200 ns	550 mW
TMM314AP-3	300 ns	550 mW
TMM314AP	450 ns	550 mW
TMM314APL-1	200 ns	385 mW
TMM314APL-3	300 ns	385 mW
TMM314APL	450 ns	385 mW

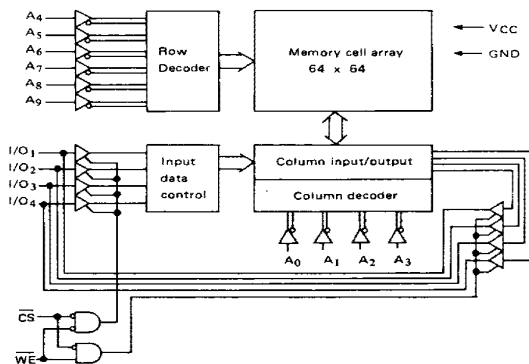
PIN CONNECTION (TOP VIEW)



PIN NAMES

$A_0 \sim A_3$	Column Address Inputs
$A_4 \sim A_9$	Row Address Inputs
$I/O_1 \sim I/O_4$	Data Input/Output
CS	Chip Select Input
WE	Write Enable Input
VCC	Supply Voltage
GND	Ground

BLOCK DIAGRAM



MAXIMUM RATINGS

SYMBOL	ITEM	RATING	UNIT
V _{CC}	Supply Voltage	-0.5 ~ 7.0	V
V _{I/O}	Input/Output Voltage	-0.5 ~ 7.0	V
T _{OPR}	Operating Temperature	0 ~ 70	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C
T _{SOLDER}	Soldering Temperature · Time	260 · 10	°C · sec
P _D	Power dissipation (T _a = 70°C)	850	mW

DC RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{IH}	Input High Voltage	—	2.0	—	V _{CC}	V
V _{IL}	Input Low Voltage	—	-0.5	—	0.8	V
V _{CC}	Supply Voltage	—	4.5	5	5.5	V

DC CHARACTERISTICS (T_a = 0 ~ 70°C)

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.*	MAX.	UNIT	
I _{IH}	Input High Current	V _{IN} = 5.50 V	—	—	10	μA	
I _{IL}	Input Low Current	V _{IN} = 0 V	—	—	-10	μA	
V _{OH}	Output High Voltage	I _{SOURCE} = -1.0 mA	2.4	2.8	—	V	
V _{OL}	Output Low Voltage	I _{SINK} = 2.1 mA	—	0.15	0.4	V	
I _{OH}	Output High Current	V _{OUT} = 2.4 V	-1.0	-5.5	—	mA	
I _{OL}	Output Low Current	V _{OUT} = 0.4 V	2.1	6.5	—	mA	
I _{LO}	Output Leakage Current	CE = V _{IH} or WE = V _{IL} V _{OUT} = 0.4 V ~ V _{CC}	—	—	± 10	μA	
I _{CC1}	Supply Current	TMM314A	25°C	—	50	64	mA
		PL/PL-1/PL-3	0°C	—	—	70	
I _{CC2}	Supply Current	TMM314A	25°C	—	70	90	mA
		P/P-1/P-3	0°C	—	—	100	

* T_a = 25°C, V_{CC} = 5V

AC CHARACTERISTICS (T_a = 0 ~ 70°C, V_{CC} = 5V ± 10%, C_L = 100pF, t_r, t_f ≤ 10 ns)**READ CYCLE**

SYMBOL	PARAMETER	TMM314AP-1/APL-1		TMM314AP-3/APL-3		TMM314AP/APL		UNIT
		MIN.	MAX.	MIN.	MAX.	MIN.	MAX.	
t _{RC}	Read Cycle Time	200	—	300	—	450	—	ns
t _{ACC}	Access Time	—	200	—	300	—	450	ns
t _{CS}	Chip Select Time	—	70	—	100	—	100	ns
t _{CX}	Output Active from CS	20	—	20	—	20	—	ns
t _{OD}	Chip Deselect Time	0	40	0	80	0	100	ns
t _{OH}	Output Hold from Address Change	20	—	20	—	20	—	ns

WRITE CYCLE

SYMBOL	PARAMETER	TMM314AP-1/APL-1		TMM314AP-3/APL-3		TMM314AP/APL		UNIT
		MIN.	MAX.	MIN.	MAX.	MIN.	MAX.	
t _{WC}	Write Cycle Time	200	—	300	—	450	—	ns
t _{WP}	Write Pulse Width	120	—	150	—	200	—	ns
t _{WR}	Write Recovery Time	0	—	0	—	0	—	ns
t _{ODW}	Output High Z from WE	0	40	0	80	0	100	ns
t _{DS}	Data Setup Time	120	—	150	—	200	—	ns
t _{DH}	Data Hold Time	0	—	0	—	0	—	ns
t _{AW}	Address to Write Setup Time	30	—	30	—	30	—	ns

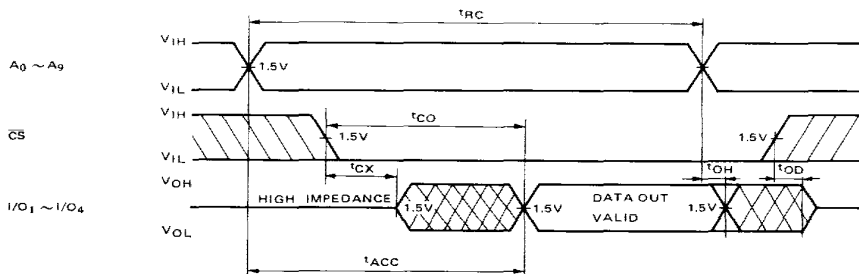
CAPACITANCE (T_a = 25°C, f = 1 MHz)

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
C _{IN}	Input Capacitance	V _{IN} = AC Ground	—	—	5	pF
C _{OUT}	Output Capacitance	V _{OUT} = AC Ground	—	—	5	pF

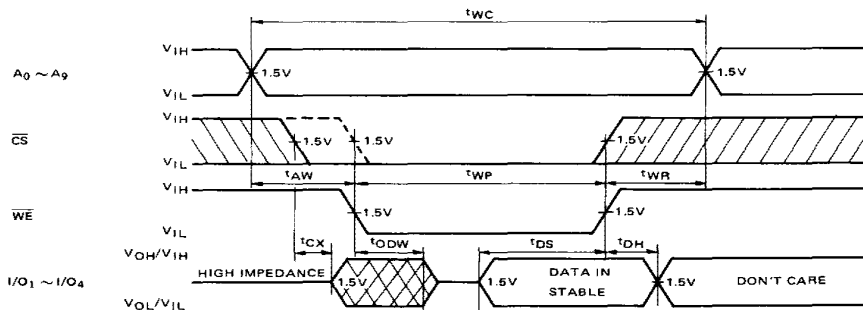
Note: This parameter is periodically sampled and not 100% tested.

TIMING WAVEFORMS

READ CYCLE



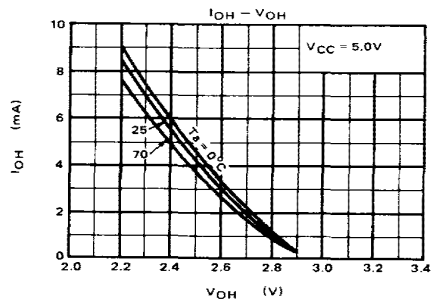
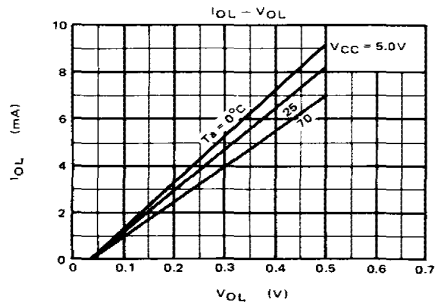
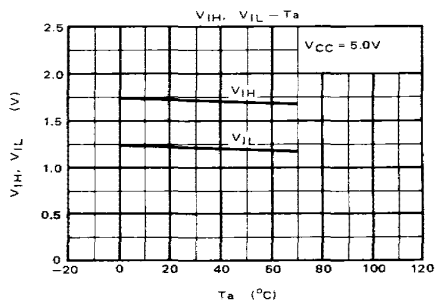
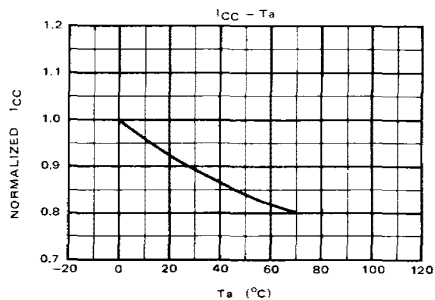
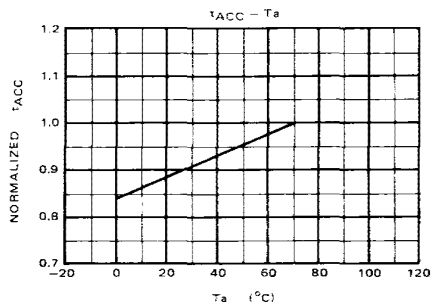
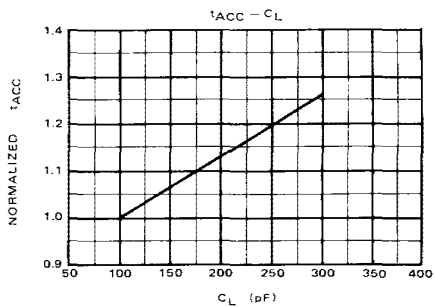
WRITE CYCLE



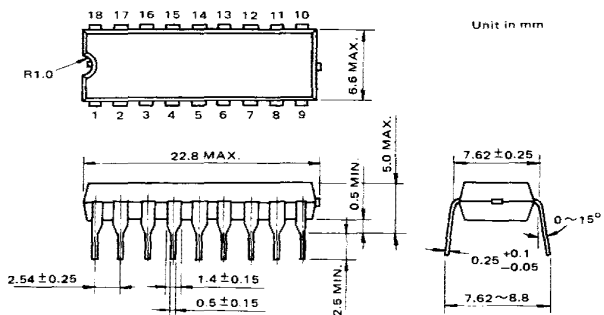
Note 1 : \overline{WE} is high for a READ CYCLE.

2 : t_{WP} is measured from the latter of \overline{CS} or \overline{WE} going low to the earlier of \overline{CS} or \overline{WE} going high.

TYPICAL CHARACTERISTICS



OUTLINE DRAWINGS



Notes: Each lead pitch is 2.54 mm. All leads are located within 0.25 mm of their true longitudinal position with respect to No. 1 and No. 18 leads.

Note: Toshiba does not assume any responsibility for use of any circuitry described; no circuit patent licenses are implied, and Toshiba reserves the right, at any time without notice, to change said circuitry.

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